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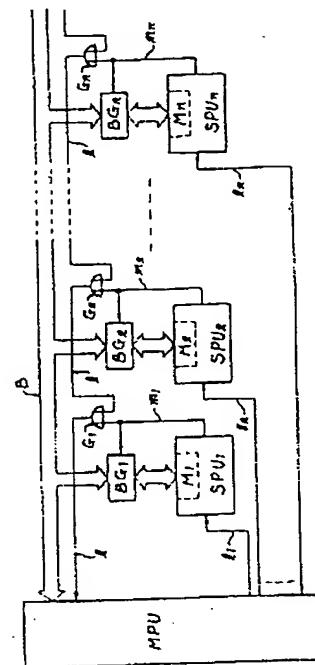
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TITLE : COMMUNICATION CONTROL SYSTEM
BETWEEN PROCESSORS



ABSTRACT : PURPOSE: To attain direct access to main control processor for the communication area of the memory of a sub ordinate control processor by sending a request for communication from the main control processor and placing the subordinate control processor in a temporary stop state.

CONSTITUTION: The main control processor MPU sends communication request information to the subordinate control processor SPU₁. The subordinate control processor SPU₁ sends communication ready information to an information line m₁ and holds itself in a temporary suspending stop state. The communication ready information of the information line m₁ opens a bus gate BG₁ corresponding to the subordinate control processor SPU₁. Consequently, the main control processor MPU attains direct access to the memory M₁ of the subordinate control processor SPU₁. When the communication ready information of the information line m₁ is ceased, the bus gate BG₁ is closed and the communication ready information of the information line l₁ is also ceased. Thus, communication control over all subordinate control processors SPU₁~SPU_n is performed.

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